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AN APPROACH TO REDUCE THE COMPUTATIONAL STEPS OF PREDICTIVE CURRENT CONTROLLER WITH CONSTANT SWITCHING FREQUENCY IN DISTRIBUTION SYSTEMS HAVING FOUR-LEG PHOTOVOLTAIC INVERTER

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ABSTRACT

Distribution systems with renewable energy sources require a neutral terminal connection between the load and inverter. This can be provided by a four-leg voltage source inverter that suppresses zero sequence harmonics when a non-linear or unbalanced load is used. Various conventional control techniques are available for current control in four-leg inverters. Among them, model predictive control shows promising performance with less control complexity. The model predictive current control has limitations in practical implementation due to the nature of its varying switching frequency and its impact in filter design. Hence a modulator is used to overcome this limitation. The modulated model predictive current control combines the advantages of predictive control like quick transient response and linear control of space vector modulation. The control objective is to optimize the modulation time ratio rather than the switching states using a cost function and thus obtain a constant switching frequency. The proposed approach aims to reduce the computational steps involved in the modulation stage. This is done by incorporating the activation order of the active voltage vectors which are calculated using simple formulas and only one look-up table. As very low value of sampling time is taken, extrapolation is avoided in the predictive model and in turn reduces the computational burden substantially. The calculations are done in one sampling period and the switching states are applied in the next instant. The performance of the same is validated through simulation carried in MATLAB/Simulink environment. Level 2 S-Function block which uses C code is used for simulation as it can accommodate discrete system and it can also be used in device drivers during hardware implementation.

Keywords— Four-leg inverter, Predictive current control, Shunt active power filter, Standalone hybrid power system, S-function, Three-dimensional space vector modulation.

I. INTRODUCTION

As renewable sources are proven to be the cleanest source of energy, ample measures are being taken to integrate the same with the distributed generation systems. Integrating the renewable sources to the grid comes with its own challenges in addition to the issues caused by load side fluctuations. Increasing the level of voltage source inverters in renewable source application leads to increase in number of switches, sensors and thus leads to complex control techniques [1]. By adding an extra leg to the conventional three-leg inverters, most of the problems arising due to unbalanced or non-linear loads can be managed. The midpoint from the fourth leg containing two switches is connected to the neutral point of the load [2]. This topological modification gives numerous advantages including flexibility in control, output voltage/current quality improvement [3].

Many applications like active front end rectifiers, hybrid distribution generation systems, shunt active power filter and electric drives can utilize the potential of current control technique in four-leg inverter [4]. Considering the non-linear characteristic nature of renewable energy sources and consequently its effect on power distribution system, a robust compensation system like a shunt active power filter having a four-leg photovoltaic inverter can be used [2]. One typical example of utilising a four-leg photovoltaic inverter as shunt active power filter in standalone power system having hybrid source of energy is presented in Fig 1.



Fig 1: Configuration of Standalone power system with renewable source, SAPF and four-leg photovoltaic inverter

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The conventional techniques to control current for a four-leg photovoltaic inverter use PID regulators followed by a duty ratio modulator to generate the gating signals [4]. Three dimensional space vector modulation is also touted to be a very comprehensible control technique for four leg inverters as it has numerous advantages of dclink utilization and minimal distortion in output [6]. Comparatively, Model predictive current control is quite an attractive control method when compared to the conventional control techniques. It shows promising effects by having fast dynamic response, robustness in adapting to the reference current, preserving a constant DC link voltage and including non-linearities while designing the discrete model of the system [7]. The only drawback faced is the variable switching frequency during operation. This leads to complication in the filter design as the filter gets bulky for a wide spectrum of harmonics [8].

By blending the advantages from both the conventional and model predictive current control together, the time interval for the switching sequence is calculated by introducing a modulator in between the control circuit and power circuit [9]. Though a modulator is present, the inner current control loop is completely avoided as predictive control does its work in the discrete model of the system using finite control set. As its name, this control technique predicts the output current of the next sampling instant and uses it to generate the required switching sequence. The switching sequence is then sent through a modulator, so that an optimized set of switching signal with constant frequency can be obtained [10].

The working of the proposed control scheme is validated by the simulations done in Simulink, Matlab. The same has been validated for various source and load conditions. This paper is organised as following sections: In Section II, the discrete-time model of the system is derived along with the mathematical explanation of the four-leg inverter topology. In Section III, the proposed control strategy with reduced computation steps is discussed along with their conventional counterparts. In Section IV, simulation results are presented and inferred followed by appropriate conclusion in Section V.

II. SYSTEM DESCRIPTION AND MODELLING OF FOUR-LEG VOLTAGE SOURCE INVERTER

A. Topology

Fig 2 shows the topology of the two-level four-leg photovoltaic inverter with voltage-source and non-linear load. R_{fa} , R_{fb} , R_{fc} , R_{fn} represent the leakage resistance of the filter. L_{fa} , L_{fb} , L_{fc} , L_{fn} represent the filter inductance. R_{fn} and L_{fn} facilitates the controllability of the zero-sequence current. R_a , R_b and R_c are taken as the load resistances.



Fig 2: Topology of typical Four-leg Photovoltaic Inverter having Voltage Source

Although the presence of additional leg in the inverter increases the complexity of the control, it can be used to handle balanced or unbalanced, linear or non-linear, and single or three-phase loads without having an impact on the dc-link capacitor. i_a , i_b , i_c , and i_n are the output load currents [8].

B. Mathematical Model

Due to the topology, four switching signals S_1 , S_2 , S_3 , S_4 are required and it leads to a total of 16 (2⁴) switching states. The instantaneous output voltages across the load of the four-leg inverter [7] can be expressed as in (1)

$$V_{gN} = R_{fg}i_g + L_{fg}\frac{di_g}{dt} + V_{nN}, \quad g = a, b, c, n$$
(1)
The output load voltages of the inverter in terms of its load parameters can be expressed as in (2)

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Where,

$$R_{eq} = \begin{bmatrix} R_a + R_{fa} + R_{fn} & R_{fn} & R_{fn} \\ R_{fn} & R_b + R_{fb} + R_{fn} & R_{fn} \\ R_{fn} & R_{fn} & R_c + R_{fc} + R_{fn} \end{bmatrix}$$

and

 $L_{eq} = \begin{bmatrix} L_{fa} + L_{fn} & L_{fn} & L_{fn} \\ L_{fn} & L_{fb} + L_{fn} & L_{fn} \\ L_{fn} & L_{fn} & L_{fc} + L_{fn} \end{bmatrix}$

The change in output current can be expressed from (1) as

$$\frac{\mathrm{di}_{g}}{\mathrm{dt}} = \frac{1}{\mathrm{L}_{\mathrm{fg}}} \left[\left(\mathrm{V}_{\mathrm{gN}} - \mathrm{V}_{\mathrm{nN}} \right) - \mathrm{R}_{\mathrm{fg}} \mathrm{i}_{\mathrm{g}} \right], \qquad \mathrm{g} = \mathrm{a}, \mathrm{b}, \mathrm{c}, \mathrm{n}$$
(3)

From (2), the load current in continuous time-domain can be expressed as

$$\frac{d}{dt} \begin{bmatrix} i_a \\ i_b \\ i_c \end{bmatrix} = A \begin{bmatrix} i_a \\ i_b \\ i_c \end{bmatrix} + B \begin{bmatrix} V_{an} \\ V_{bn} \\ V_{cn} \end{bmatrix}$$
(4)

Where

$$A = L_{eq}^{-1} R_{eq}, \qquad B = L_{eq}^{-1}$$

III. Improved Modulated MPCC with Reduced Computations

The block diagram of the Improved Modulated MPCC for a three-phase four-leg photovoltaic inverter [9] having resistive (R) load is shown in Fig 3. For easy understanding, the conventional MPCC and MPCC with constant switching frequency are discussed first followed by the improved method of Modulated MPCC with reduced computations.



Fig 3: Block Diagram of Improved Modulated MPCC of four-leg Photovoltaic Inverter

A. Conventional Model Predictive Current Control (MPCC)

For the design process and digital implementation, the load currents in state-space representation and discretetime model is required. The voltage applied at the output RL filter can be written as shown in (5)

$$\begin{bmatrix} V_{an} \\ V_{bn} \\ V_{cn} \end{bmatrix} = \begin{bmatrix} S_1 - S_4 \\ S_2 - S_4 \\ S_3 - S_4 \end{bmatrix} V_{dc}$$
(5)

The design procedure of the conventional Model Predictive Current control are as follows.

Step 1. Required Measurements

The output load current and voltage across DC-link capacitor are the required input components of the feedback system. For this, three current sensor across each phase and one voltage sensor across DC-link

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capacitor is required. The output load voltage in terms of DC-link voltage and switching signals can be easily calculated from (4).

Step 2. Generation and Extrapolation of Reference currents

The future prediction of load current reference is calculated using fourth-order Lagrange extrapolation. The fourth-order is utilized for a broad range of frequencies of reference output current, i*. The calculation of future load current is done as follows.

$$i^{*}[k+1] = 4i^{*}[k] - 6i^{*}[k-1] + 4i^{*}[k-2] - i^{*}[k-3]$$
(6)

Step 3. Formulation of Predictive Load currents

The formulation of predictive load currents is done by considering both the converter and load in the discretetime domain. Additional constraints like sampling time and approximations can be included in the models. A recursive matrix equation is derived for the discrete-time model and it calculates the system prediction. The system states at any instant $(k+1)T_s$ for a given sampling time, T_s can be predicted with the feedback components known at instant $(k)T_s$. The recursive equation is formulated as shown in (7)

$$i[k+n+1] = Ji[k+n] + Kv[k+n]$$
 (7)

Where

$$J = \begin{bmatrix} j_1 & j_2 & j_3 \\ j_4 & j_5 & j_6 \\ j_7 & j_8 & j_9 \end{bmatrix} = e^{AT_s}$$
$$K = \begin{bmatrix} k_1 & k_2 & k_3 \\ k_4 & k_5 & k_6 \\ k_7 & k_8 & k_9 \end{bmatrix} = A^{-1}(J - I_{3x3})B$$

Since matrices of order 3 are involved, to reduce the computation for every sampling instant, the values are calculated offline and then used in prediction algorithm.

Step 4. Estimation of Cost Function

The space vector diagram of four-leg inverter contains 6 prisms with 4 tetrahedrons each. So, 24 tetrahedrons are present that enclose 16 switching vectors of the photovoltaic inverter. Each tetrahedron has 3 active voltage vectors. The active voltage vectors are V_1 to V_{14} and produce non-zero output voltage. V_0 and V_{15} are the zero-voltage vectors and they produce no output voltage. By selecting them appropriately in a switching sequence, the switching frequency can be reduced. The switching states along with their corresponding voltage vectors are highlighted in Table 1. With 16 (24) combination of the switching signals, 16 predictions of the load current are obtained.

Table 1: Voltage Vectors and Switching States for Four-Leg Photovoltaic Inverter

V	С	S_1	S_2	S_3	S_4
V_0	c_0	0	0	0	0
V_1	c ₁	0	0	1	0
V_2	c ₂	0	1	0	0
V_3	c ₃	0	1	1	0
V_4	c ₄	1	0	0	0
V_5	c ₅	1	0	1	0
V_6	c ₆	1	1	0	0
V_7	c ₇	1	1	1	0
V_8	c ₈	0	0	0	1
V_9	C 9	0	0	1	1
V_{10}	c ₁₀	0	1	0	1
V ₁₁	c ₁₁	0	1	1	1
V ₁₂	c ₁₂	1	0	0	1
V ₁₃	c ₁₃	1	0	1	1
V ₁₄	c ₁₄	1	1	0	1
V ₁₅	c ₁₅	1	1	1	1

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(9)

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The control objective of the predictive current control is achieved by formulating the cost function. The 16 predictions for i[k+1] are found using (7) and then compared with their respective reference currents. The comparison yields the cost function value. Cost function, c is found as follows

$$c[k+1] = (i_a^*[k+1] - i_a[k+1])^2 + (i_b^*[k+1] - i_b[k+1])^2 + (i_c^*[k+1] - i_c[k+1])^2$$
(8)

$$c[k + 1] = ||i^*[k + 1] - i[k + 1]||^2$$

The formulation in (8) leads to 16 different cost function values pertaining to the 16 voltage vectors in Table 1. The real work of minimal cost function block is to gather the minimum value of cost function and identify its corresponding switching state. For example, if cost function c_5 has a minimal value among the calculated values of c_0 to c_{15} , the switching state combination corresponding to c_5 is taken as the optimum choice and applied directly to the four-leg inverter. The conventional predictive current control does not have a modulating stage and it causes variable switching frequency during implementation.

B. MPCC WITH CONSTANT SWITCHING FREQUENCY

Step 5. Calculation of Modulation Time Ratio

The constant switching frequency can be obtained in Predictive Current Control by adding a modulator. The control objective of the controller now shifts from optimizing the switching states to optimizing the modulation time ratio. For this, 3 active voltage vectors and 1 zero-voltage vector (V_i , V_j , V_k and V_0) and their corresponding cost function values (c_i , c_j , c_k and c_0). The relationship between Voltage vectors, duty cycle and the cost function are tabulated exhaustively in Table 2.

 Table 2: Adjacent Voltage Vectors, Duty Cycles and Cost Functions in the 24 Tetrahedrons (T) of a Four-Leg

 Photovoltaic Inverter

T	W	V ₀	Vi	Vj	V _k	\mathbf{d}_0	di	dj	d _k	c ₀	ci	cj	C _k
1	W_0	V_0	V_4	V ₁₂	V ₁₄	d_0	d ₄	d ₁₂	d ₁₄	c ₀	c_4	c ₁₂	c ₁₄
2	W ₁	V_0	V_4	V_6	V ₁₄	d_0	d ₄	d ₆	d ₁₄	c_0	c_4	c ₆	c ₁₄
3	W_2	V_0	V_4	V_6	V_7	d_0	d_4	d_6	d_7	c_0	c_4	c ₆	c ₇
4	W ₃	V_0	V_8	V ₁₂	V ₁₄	d_0	d_8	d ₁₂	d ₁₄	c_0	c_8	c ₁₂	c ₁₄
5	W_4	V_0	V_3	V_6	V ₁₄	d_0	d_3	d_6	d ₁₄	c_0	c ₃	c ₆	c ₁₄
6	W_5	V_0	V_3	V ₁₀	V ₁₄	d_0	d_3	d ₁₀	d ₁₄	c_0	c ₃	c ₁₀	c ₁₄
7	W ₆	V_0	V_3	V_6	V_7	d_0	d_3	d ₆	d ₇	c_0	c ₃	c ₆	c ₇
8	W_7	V_0	V_8	V ₁₀	V ₁₄	d_0	d_8	d ₁₀	d ₁₄	c_0	c_8	c ₁₀	c ₁₄
9	W_8	V_0	V_3	V ₁₀	V ₁₁	d_0	d_3	d ₁₀	d ₁₁	c_0	c ₃	c ₁₀	c ₁₁
10	W ₉	V_0	V_3	V_3	V ₁₁	d_0	d ₃	d ₃	d ₁₁	c_0	c ₃	c ₃	c ₁₁
11	W ₁₀	V_0	V_3	V_3	V_7	d_0	d_3	d ₃	d ₇	c_0	c ₃	c ₃	c ₇
12	W ₁₁	V_0	V_8	V_{10}	V ₁₁	d_0	d_8	d ₁₀	d ₁₁	c_0	c_8	c ₁₀	c ₁₁
13	W ₁₂	V_0	V_1	V_3	V ₁₁	d_0	d_1	d ₃	d ₁₁	c_0	c_1	c ₃	c ₁₁
14	W ₁₃	V_0	V_1	V_9	V ₁₁	d_0	d_1	d ₉	d ₁₁	c_0	c_1	c ₉	c ₁₁
15	W ₁₄	V_0	V_1	V_3	V_7	d_0	d_1	d ₃	d ₇	c_0	c ₁	c ₃	c ₇
16	W ₁₅	V_0	V_8	V_9	V ₁₁	d_0	d_8	d ₉	d ₁₁	c_0	c_8	C9	c ₁₁
17	W ₁₆	V_0	V_1	V_9	V ₁₃	d_0	d_1	d_9	d ₁₃	c_0	c ₁	c ₉	c ₁₃
18	W ₁₇	V_0	V_1	V_5	V ₁₃	d_0	d_1	d_5	d ₁₃	c_0	c ₁	C ₅	c ₁₃
19	W ₁₈	V_0	V_1	V_5	V_7	d_0	d_1	d_5	d ₇	c_0	c_1	C ₅	c ₇
20	W ₁₉	V_0	V_8	V_9	V ₁₃	d_0	d_8	d ₉	d ₁₃	c_0	c_8	C 9	c ₁₃
21	W ₂₀	V_0	V_4	V_5	V ₁₃	d_0	d_4	d ₅	d ₁₃	c_0	c_4	c ₅	c ₁₃
22	W ₂₁	V_0	V_4	V ₁₂	V ₁₃	d_0	d_4	d ₁₂	d ₁₃	c_0	c_4	c ₁₂	c ₁₃
23	W ₂₂	V_0	V_4	V_5	V_7	d_0	d_4	d ₅	d ₇	c_0	c_4	c ₅	c ₇
24	W ₂₃	V_0	V_8	V ₁₂	V ₁₃	d_0	d_8	d ₁₂	d ₁₃	c_0	c_8	c ₁₂	c ₁₃

The sixteen cost function values listed in Table 1 are used to determine the modulation ration for the stationary voltage vectors as follows

$$d_i = \frac{c_j c_k c_0}{c_i c_j c_k + c_j c_k c_0 + c_i c_j c_0 + c_i c_k c_0}$$

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$$\begin{split} d_{j} &= \frac{c_{j}c_{k}c_{0}}{c_{i}c_{j}c_{k} + c_{j}c_{k}c_{0} + c_{i}c_{j}c_{0} + c_{i}c_{k}c_{0}} \\ d_{k} &= \frac{c_{i}c_{j}c_{0}}{c_{i}c_{j}c_{k} + c_{j}c_{k}c_{0} + c_{i}c_{j}c_{0} + c_{i}c_{k}c_{0}} \\ d_{0} &= \frac{c_{j}c_{j}c_{k}}{c_{i}c_{j}c_{k} + c_{j}c_{k}c_{0} + c_{i}c_{j}c_{0} + c_{i}c_{k}c_{0}} \end{split}$$

Step 6. Selection of Switching Sequence

After calculating the modulation ratio of the space vectors, it is necessary to pick the appropriate switching sequence. The switching sequence has no effect on the output voltage but it has effect on the harmonic spectral content of the inverter output and power losses. Symmetrical switching sequence is selected as it has proper symmetry within the modulation time period and it consequently shapes the desired harmonic spectrum produced in the inverter output [11].

Step 7. Modified Cost Function

A modified cost function is used to calculate the error in load current in each tetrahedron of the photovoltaic inverter.

$$W(k) = d_ig_i + d_jg_j + d_kg_k + d_0g_0$$

(10)

After calculating 24 different cost function that optimizes duty-cycle (W_0 to W_{23}) as shown in Table 2, the optimal stationary voltage vectors and duty cycles corresponding to the minimal cost function W is selected and the appropriate switching sequence is applied [12].

C. Improved Modulated MPCC with Reduced Computations

The symmetrically aligned switching sequence pattern has both zero vectors V0 and V15 within the modulation period. A nine-step switching sequence is selected and the time duration of each leg switching time is calculated.

$$T_{a} = \Delta T_{sw} \left(\frac{d_{0}}{4}\right)$$
$$T_{b} = \Delta T_{sw} \left(T_{a} + \frac{d_{i}}{2}\right)$$
$$T_{c} = \Delta T_{sw} \left(T_{b} + \frac{d_{j}}{2}\right)$$
$$T_{0} = \Delta T_{sw} \left(T_{c} + \frac{d_{0}}{2}\right)$$

The activating order of each time duration can be found from the selected active vectors. The calculation in reduces the computational steps involved. Also, in order to avoid overmodulation or discontinuous pulse width modulation, the active vectors can be projected on the $\alpha\beta$ and a cylindrical boundary can be maintained by checking ($|v_{\alpha\beta0}*| \leq V_{dc}$). The zero-sequence voltage obtained from the fourth leg can also be limited by setting a spherical boundary by checking

$$\sqrt{2} |\mathbf{v}_{\alpha\beta0}^*| + |\mathbf{v}_0^*| \le \sqrt{3} \mathbf{V}_{dc}$$

IV. DISCUSSIONS AND SIMULATION STUDIES

The proposed system has been validated through a simulation study on four-leg photovoltaic inverter in MATLAB/Simulink environment with the parameters mentioned in Table 3. The simulation model uses level 2 S-function block for computation. The S-function block in written in C code and then compiled as MEX file. Since S-Function is a user defined block, it can accommodate discrete system. It instructs the mathematical engine on what to do during initialization, update, output and termination. This particular distinct quality of S-function block is used to implement the prediction model of k+1 instant in kth instant.

At the same time, the switching states after modulator stage can be updated to the power converter switches. This compensates the delay developed during the modulation stage. It also eases the digital implementation and can be easily incorporated in hardware device drivers. The simulation result for the proposed system with unbalanced reference currents and loads are studied and validated. It is compared with the conventional predictive current control model and their THD components are also discussed.

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Table 3: Parameters of the Simulated System					
Symbol	Parameter	Values			
V _{dc}	DC link voltage	590 V			
C _{dc}	DC capacitor	1000 µF			
$\mathbf{L}_{\mathbf{f}}$	Filter inductance	5 mH			
$\mathbf{R}_{\mathbf{f}}$	Filter resistance	0.5 Ω			
Ts	Sampling time	20 µs			
\mathbf{f}_{0}	Load frequency	50 Hz			
R _a	Phase-a load resistance	50 Ω			
R _b	Phase-b load resistance	50 Ω			
R _c	Phase-c load resistance	50 Ω			

A. Step change in Unbalanced Reference Currents and Loads

The reference currents are changed with a step change at $t = 0.5f_0$ (load frequency) as shown in Fig 4. The performance of the conventional MPCC at switching frequency $T_{sw} = 2.2$ kHz and the improved modulated MPCC with switching frequency $T_{sw} = 10$ kHz is shown in Fig 4(a) and Fig 4(b). As the modified cost function optimizes the modulation time ratio, the switching frequency is taken as 10 kHz. There is no overshoot observed in the step change at 0.01 second. The load currents also show better tracking performance and high transient response. Neutral current is developed in the system due to the load current unbalance and it flows through the additional leg of the inverter. The inverter line-line voltage can also be observed to have two levels. The proposed system is compared with the conventional MPCC where the sampling time is taken as 20 µs which is slightly reduced than the normal value.



Fig 4 Simulation result for step change with unbalanced loads and reference currents: (i) Output current (A), (ii) Neutral current (A), (iii) Output line – line voltage (V)

The error in tracking is slightly observed as the optimal tetrahedron containing three active voltage vectors and zero voltage vector is selected during each sampling period rather than the optimized switching states. This happens because, the tetrahedron with three active voltage vectors are selected instead of one vector corresponding to the optimized switching state in conventional MPCC.



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The THD analysis on phase-b load current is shown in Fig 5. Concentrated harmonics can be observed in Fig 5(b) at switching frequency, F_{sw} and its higher orders. This simplifies the process of designing a suitable filter for eliminating the harmonics content.

Considering the application of four-leg photovoltaic inverter in shunt active power filter for standalone hybrid power systems, the proposed scheme is tested for non-sinusoidal and trapezoidal reference currents as demonstrated in Fig 6. The loads are the same for both the conditions shown in Fig 6. For non-sinusoidal reference current, third order harmonic in injected. The simulation results show a good tracking capacity of the proposed system with a fast dynamic response. The neutral current discharges through the additional leg of the photovoltaic inverter due to non-sinusoidal reference currents.



b) Trapezoidal reference currents

Fig 6 Simulation result of proposed system with non-sinusoidal and trapezoidal reference currents: (i) Output currents (A), (ii) Neutral current (A)

The simulation result of the proposed system with unbalanced and balanced reference current is shown in Fig 7. The optimal tetrahedron selection is also shown. It changes from 0 to 12 as balanced load is considered under steady-state. The simulation result of the proposed system ramp change in the balanced reference current is shown in Fig 8. The optimized tetrahedron is seen to change among 1 to 24 due to the ramp change in reference currents.



Fig 7 Simulation result with balanced and unbalanced loads and reference currents: (i) Output Current (A), (ii) Optimized tetrahedron selection

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b) Ramp change in in balanced reference current and unbalanced load condition

Fig 8 Simulation result with ramp change in unbalanced and balanced load conditions (i) Output Current (A), (ii) Optimized tetrahedron selection

CONCLUSION

An approach to reduce the number of computational steps in the modulated model predictive current control of the four-leg photovoltaic inverter is discussed in this paper. The proposed control strategy uses a cost function that optimizes the tetrahedron containing four voltage vectors and generate switching signals corresponding to the optimized tetrahedron. The operating principle from conventional MPCC and MPCC with constant switching frequency is combined and the computational steps of referring to the lookup table can be reduced by modifying the process of calculating the application interval from the optimized duty cycle values. A symmetrical type of switching sequence is selected as they limit the presence of harmonic content in higher frequencies. Most of the harmonic components are dominant around the constant switching frequency. The proposed system ensures fast dynamic response, low sampling rate due to reduced computational steps, low steady-state error and constant switching frequency.

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